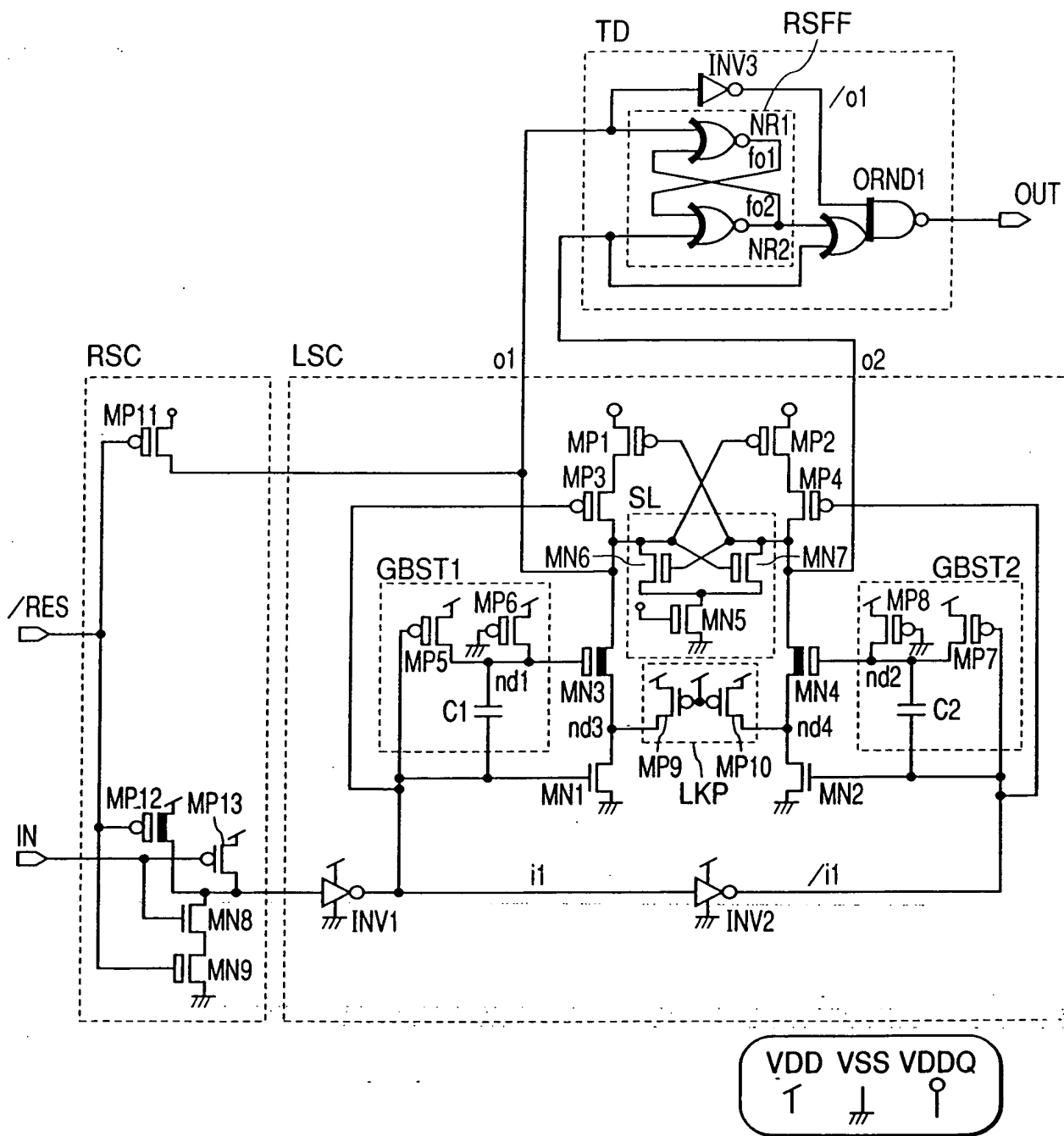






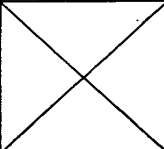
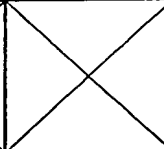


FIG. 1



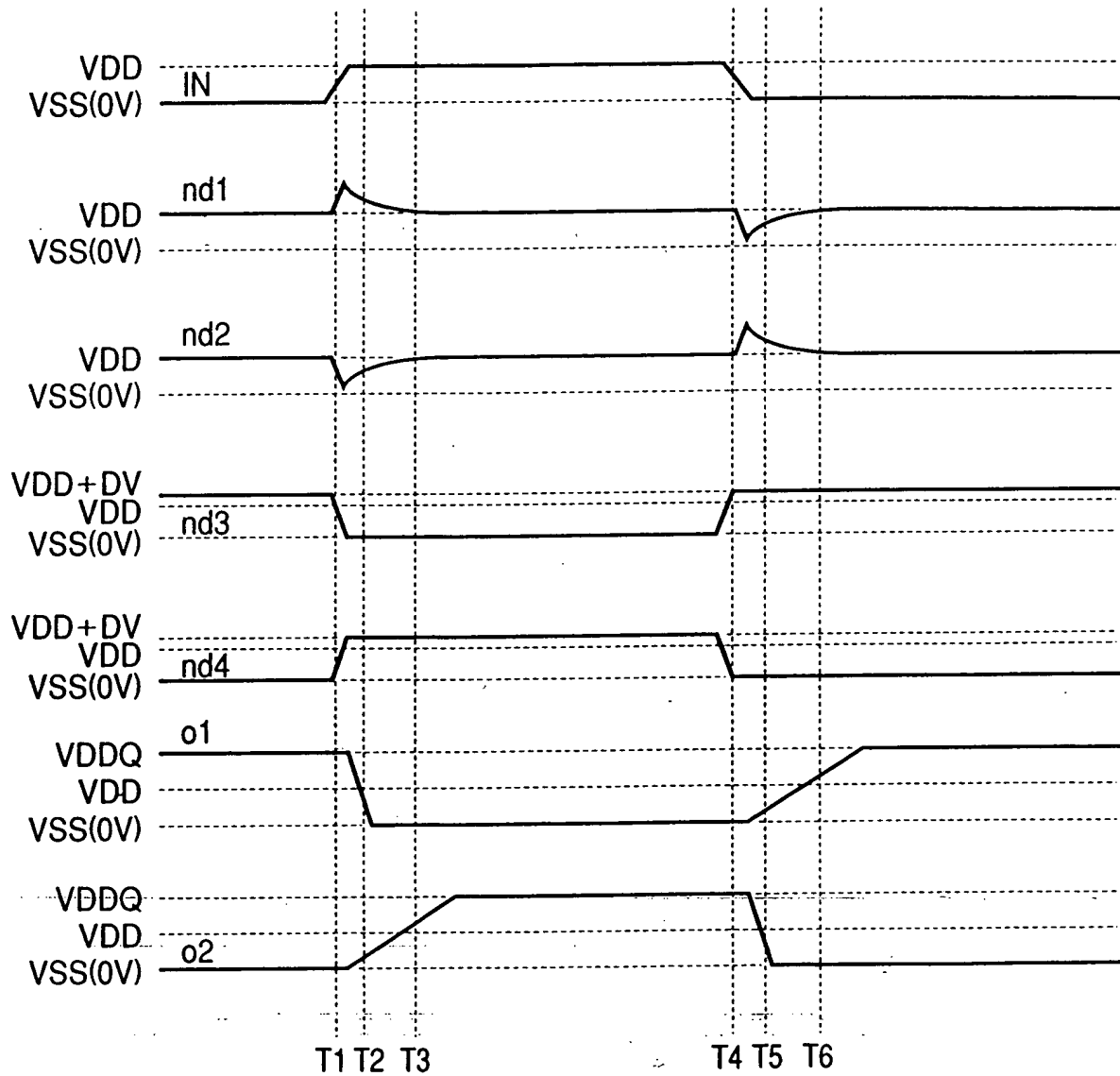
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FIG. 2

	PMOS	NMOS	PMOS	NMOS
THICK-FILM MOS (HIGH WITHSTAND VOLTAGE)				
THIN-FILM MOS (LOW WITHSTAND VOLTAGE)				
	STANDARD THRESHOLD MOS		LOW THRESHOLD MOS	

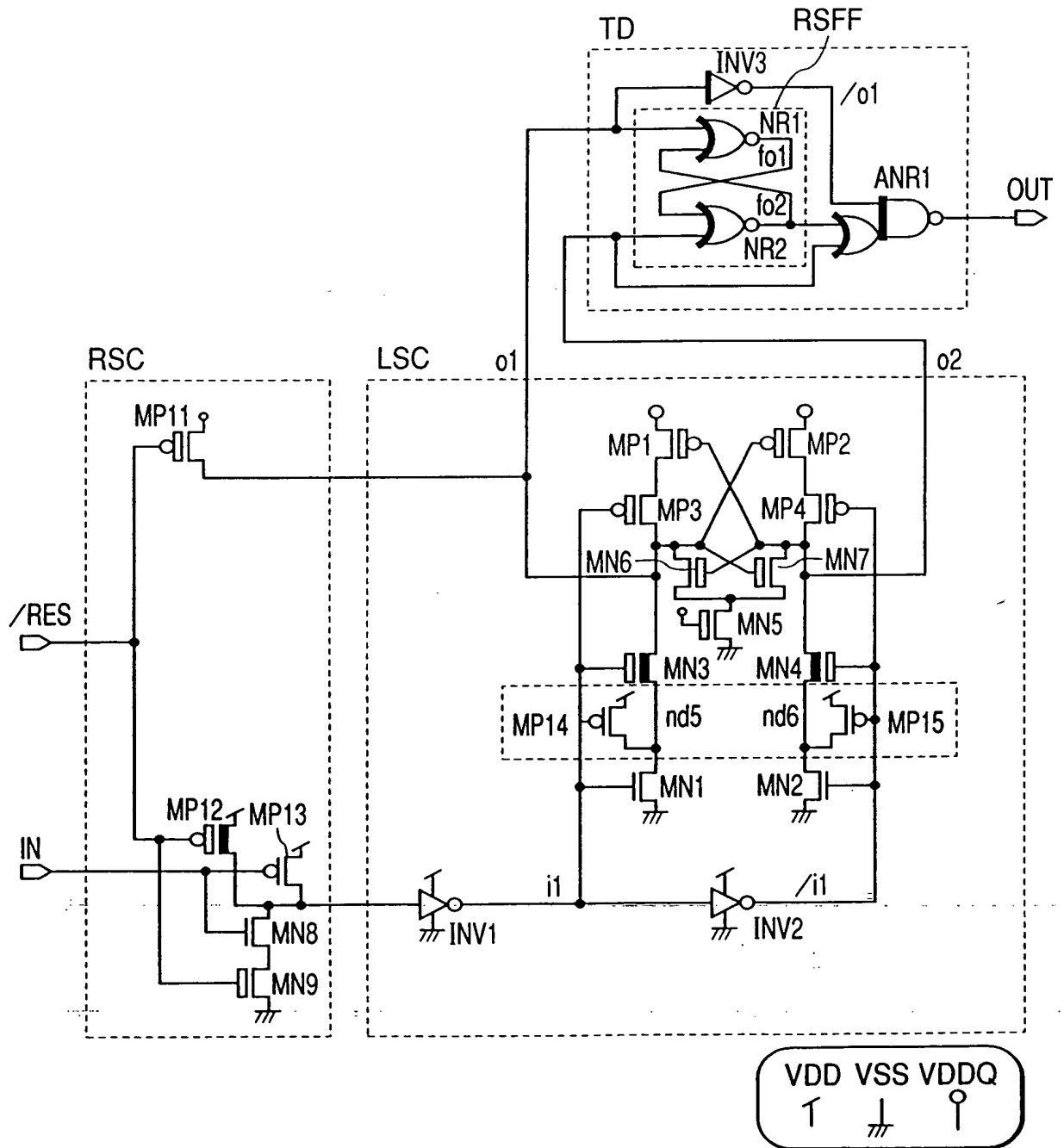
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*FIG. 3*



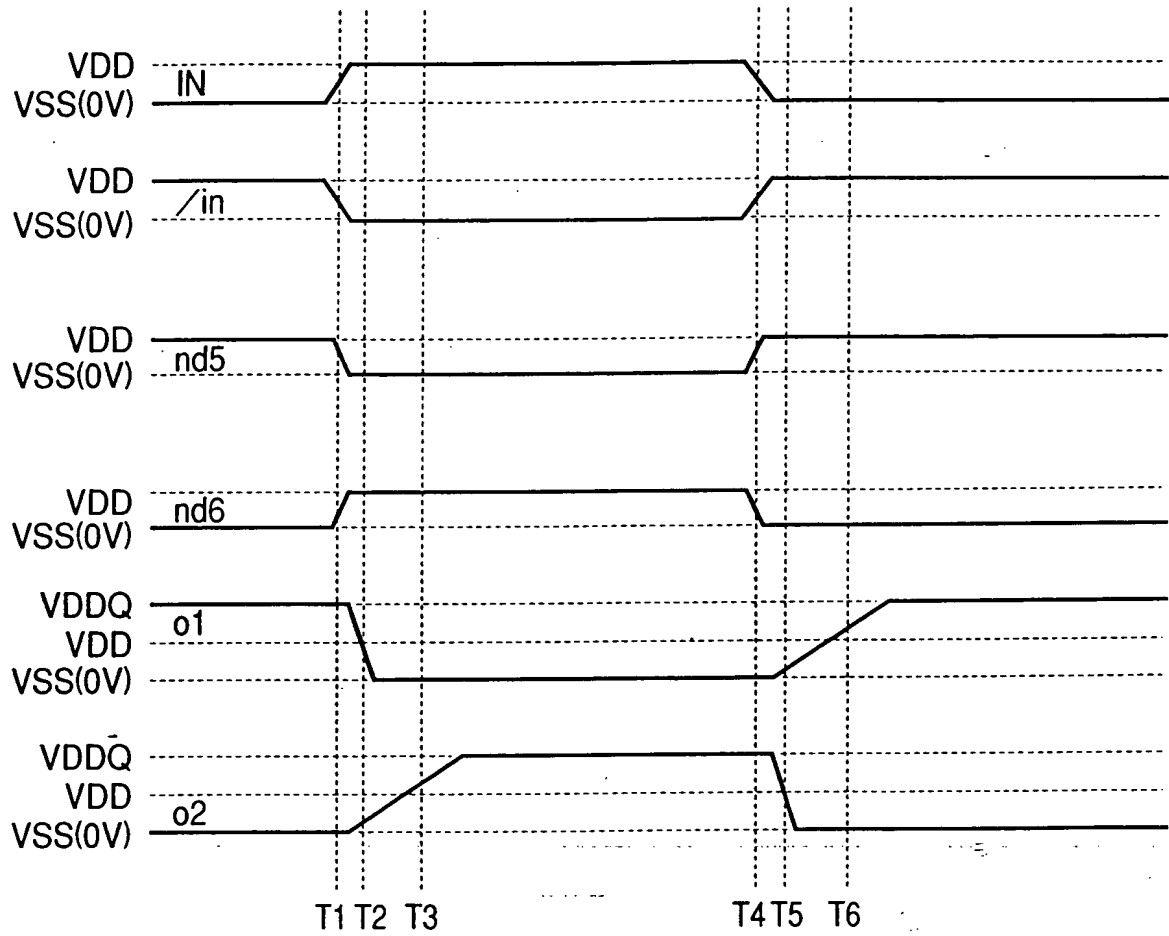
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FIG. 4



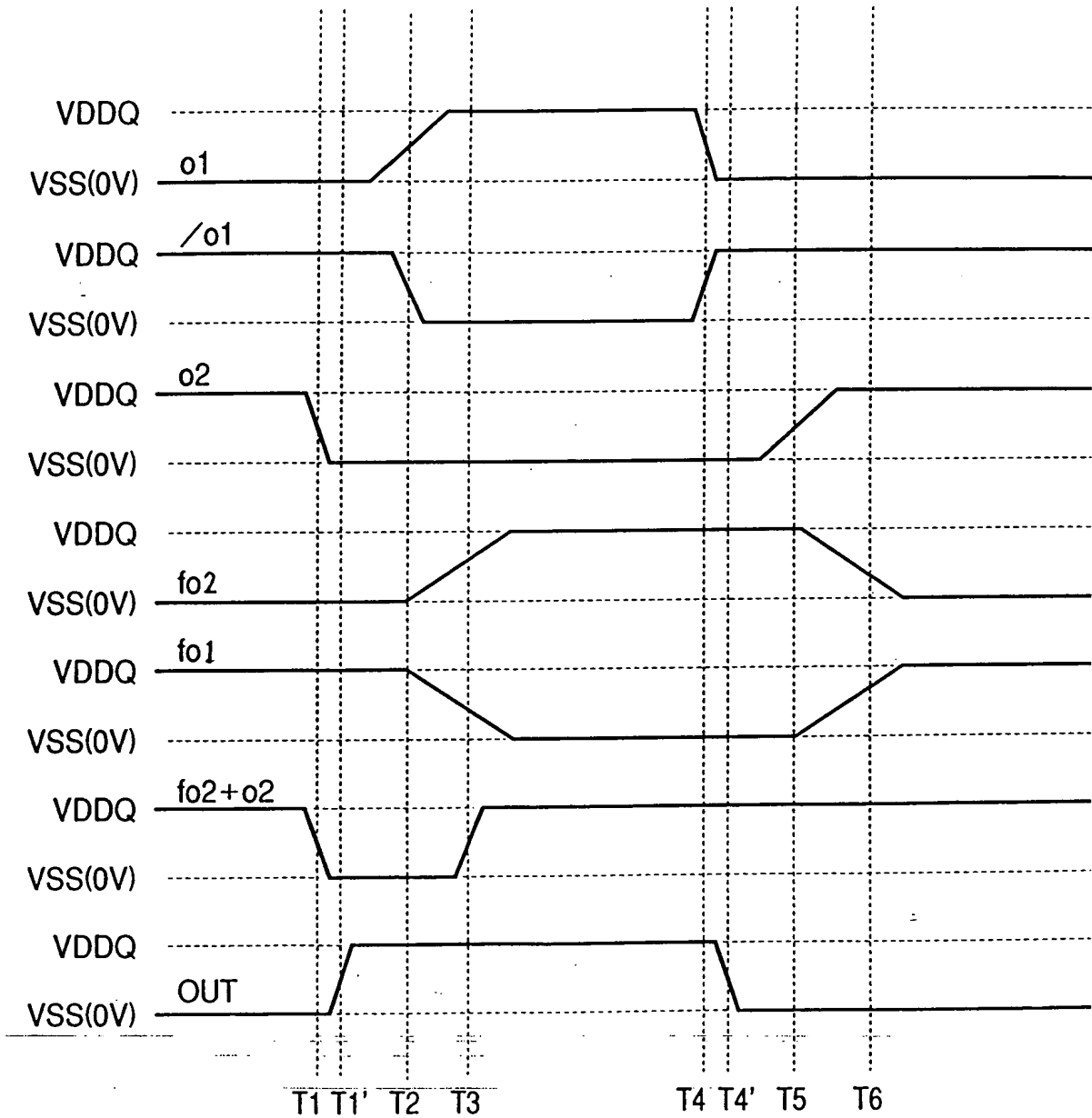
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*FIG. 5*



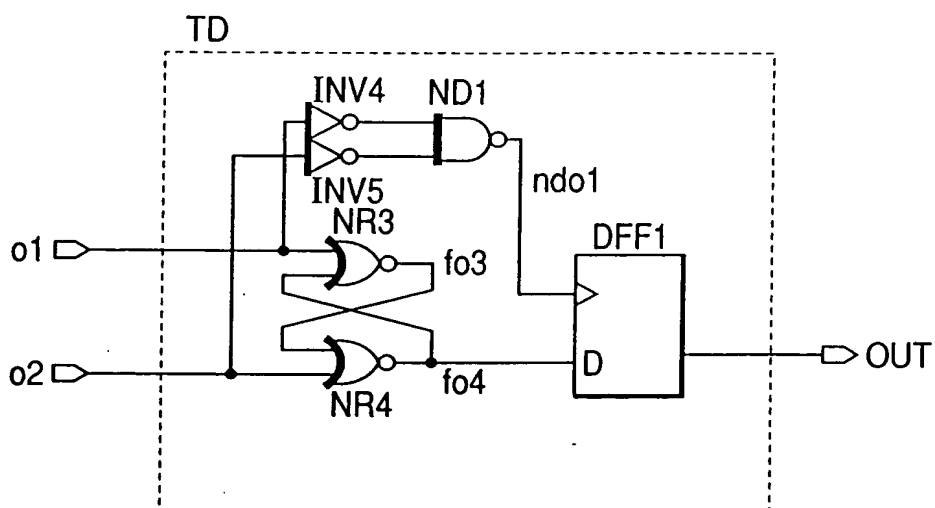
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FIG. 6



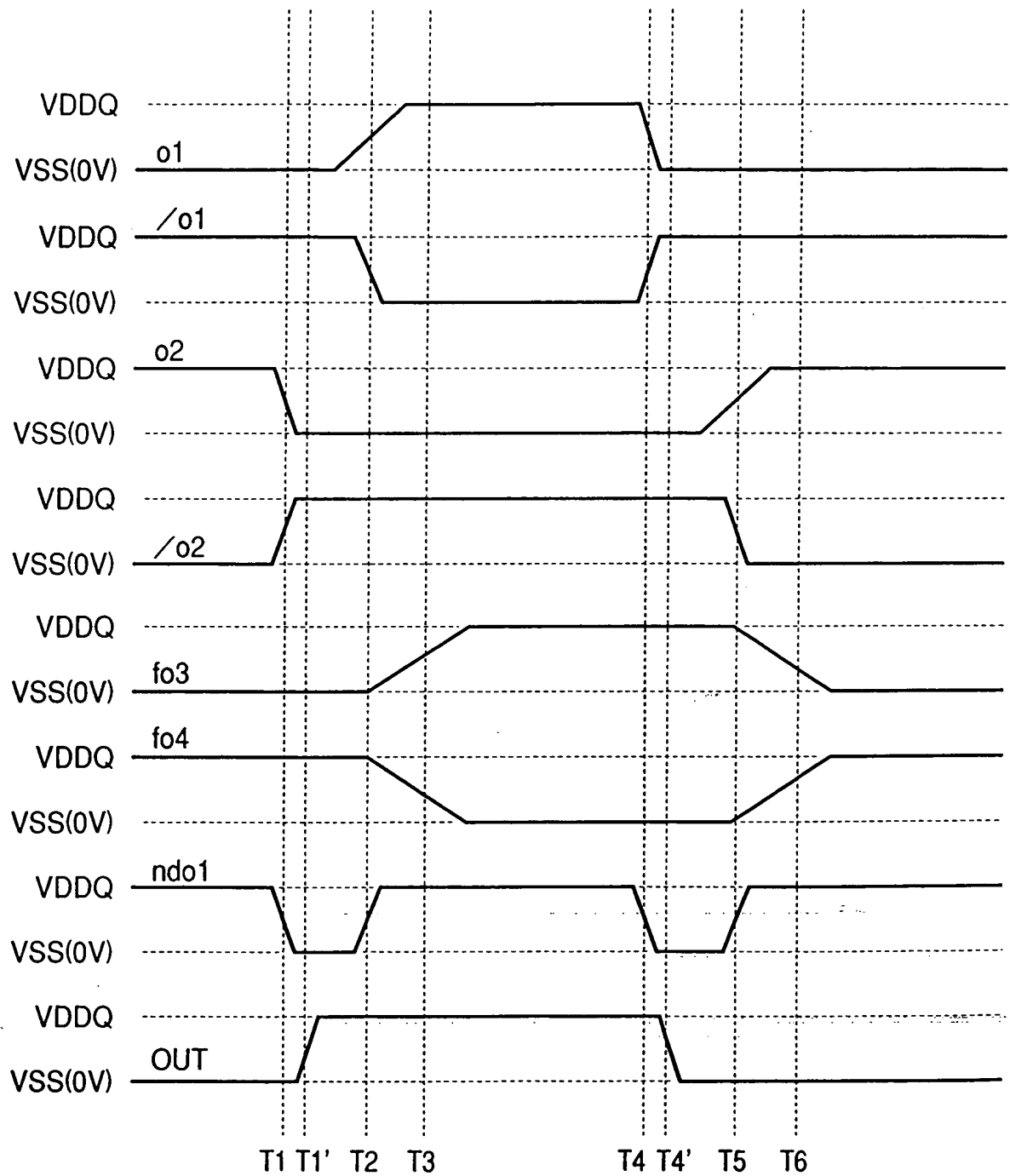
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**FIG. 7**



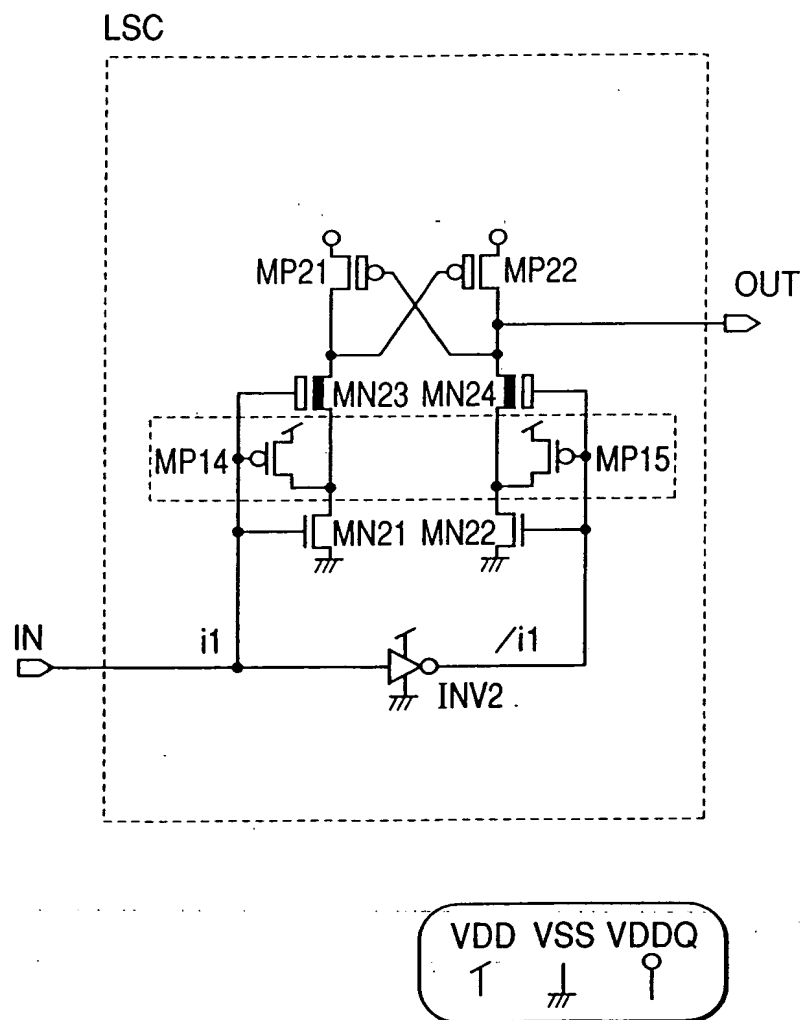
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*FIG. 8*



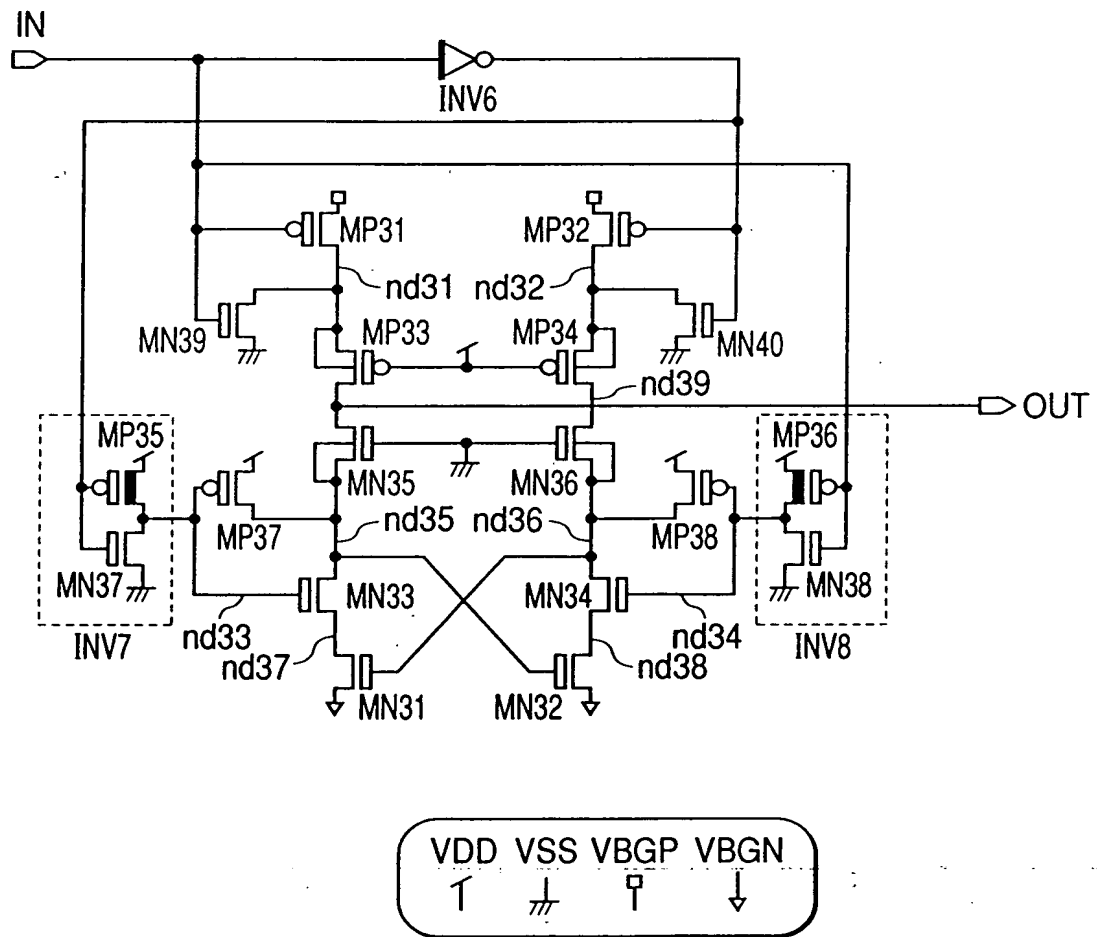


**FIG. 9**



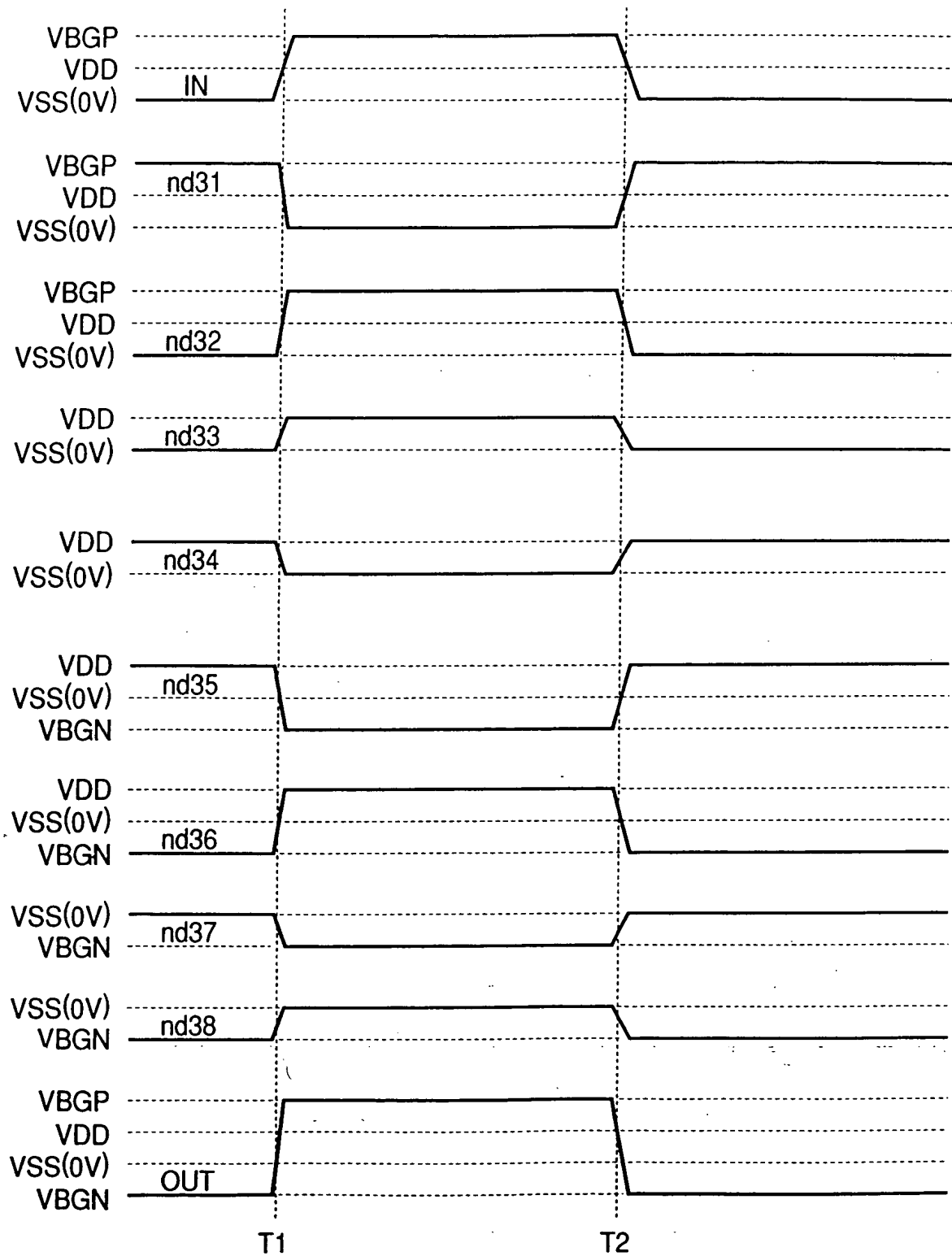
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FIG. 10

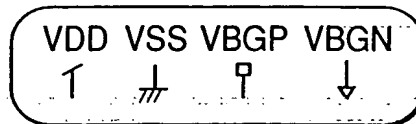
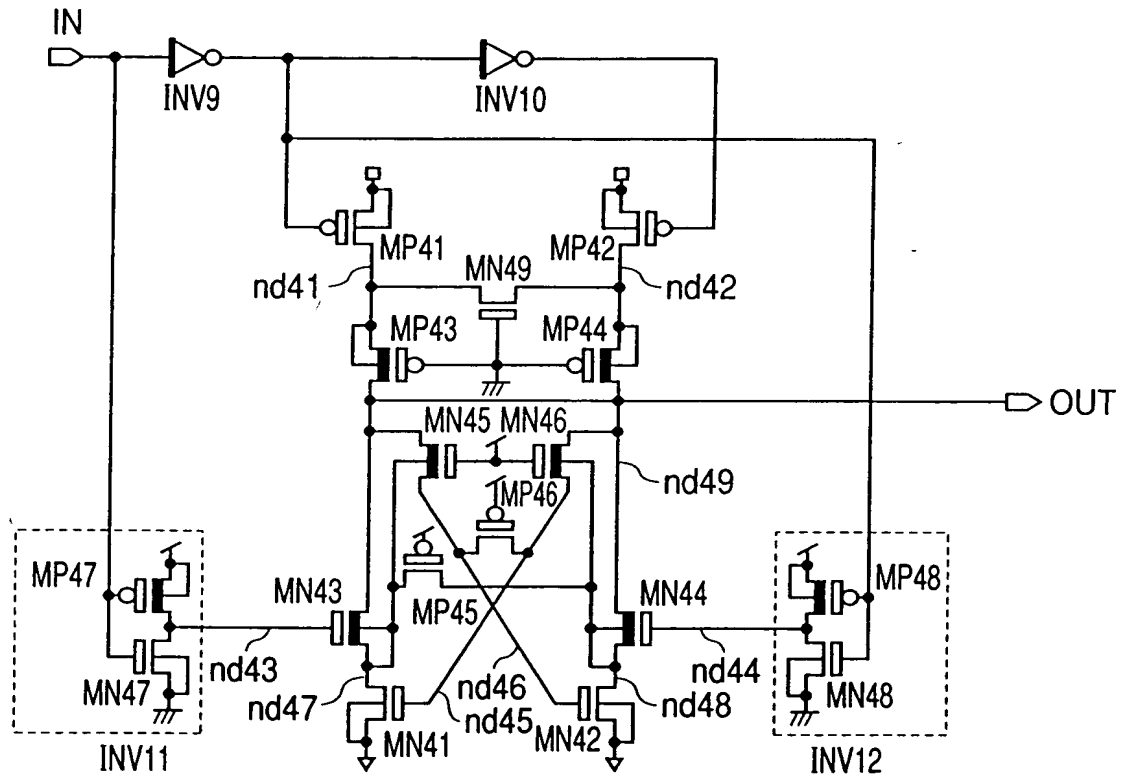


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**FIG. 11**



*FIG. 12*



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FIG. 13

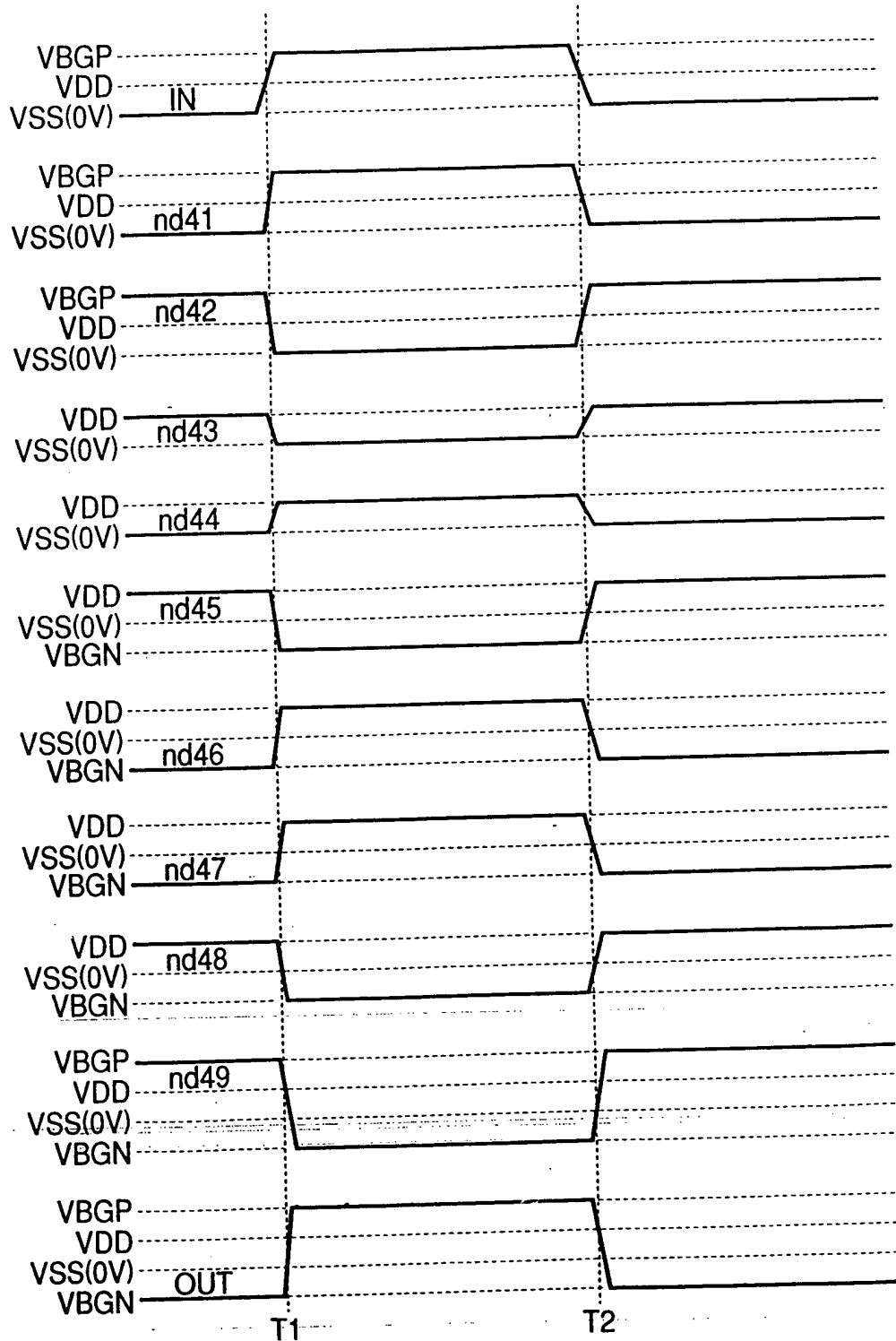
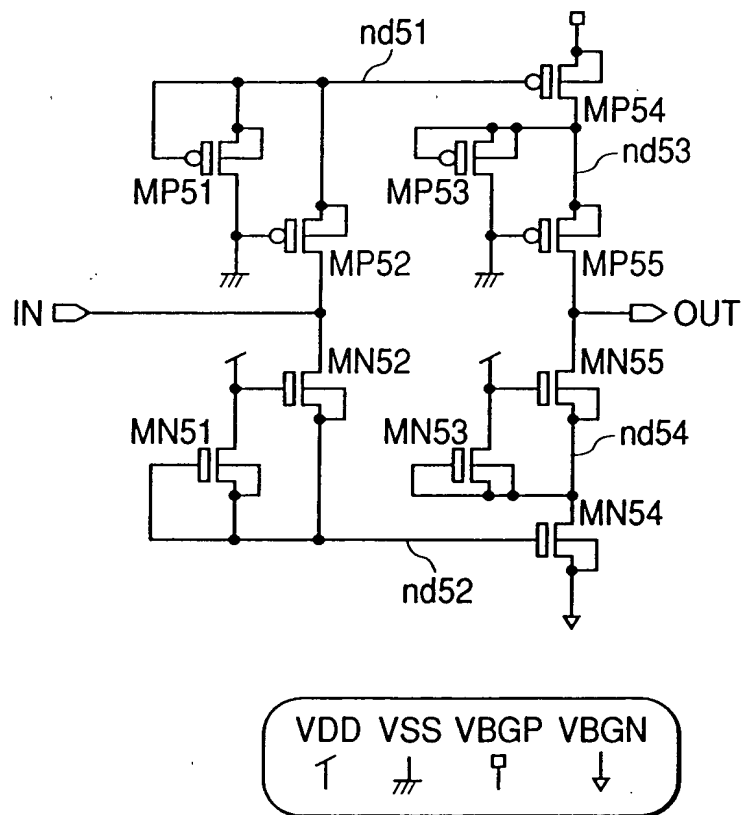
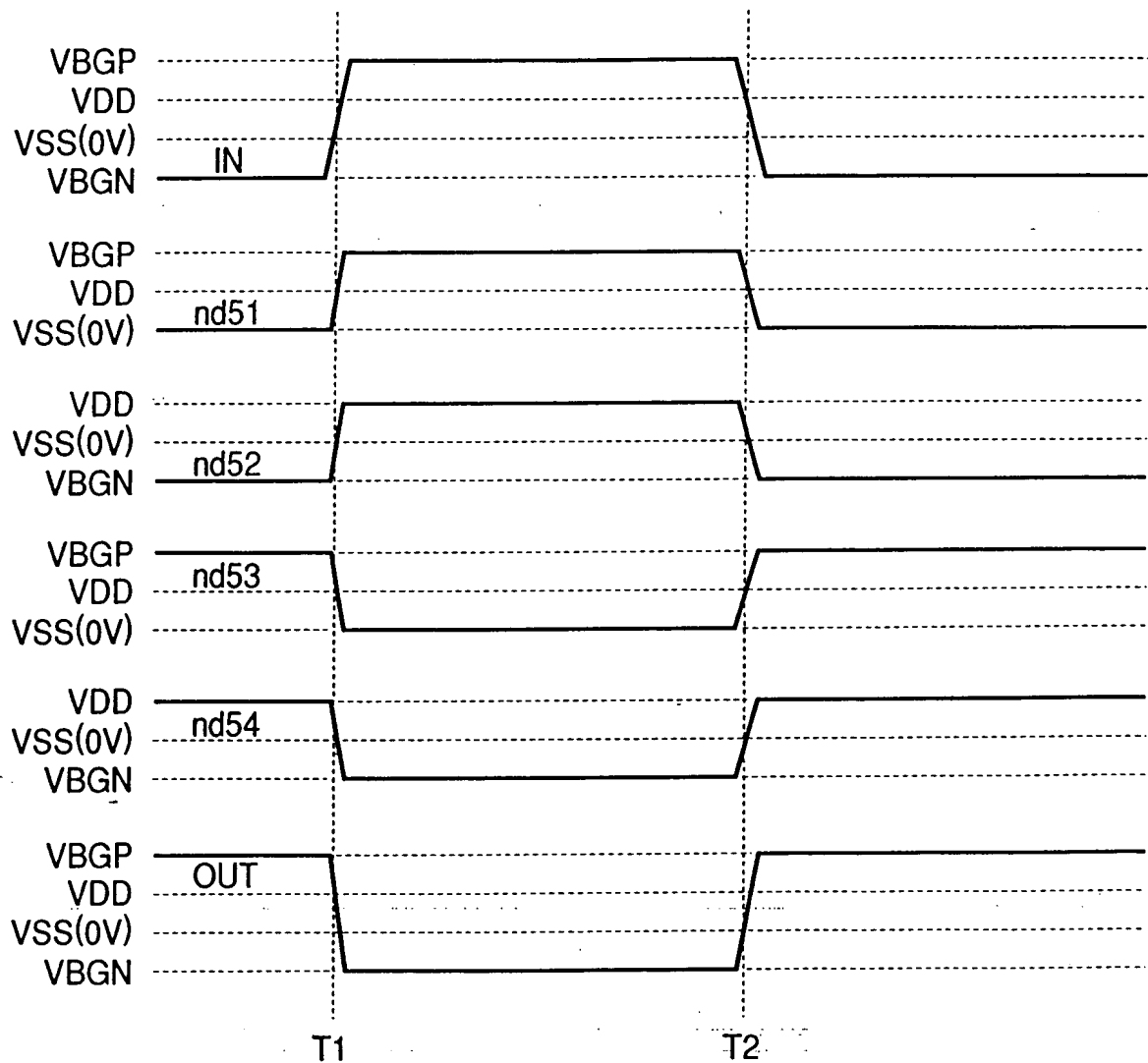


FIG. 14



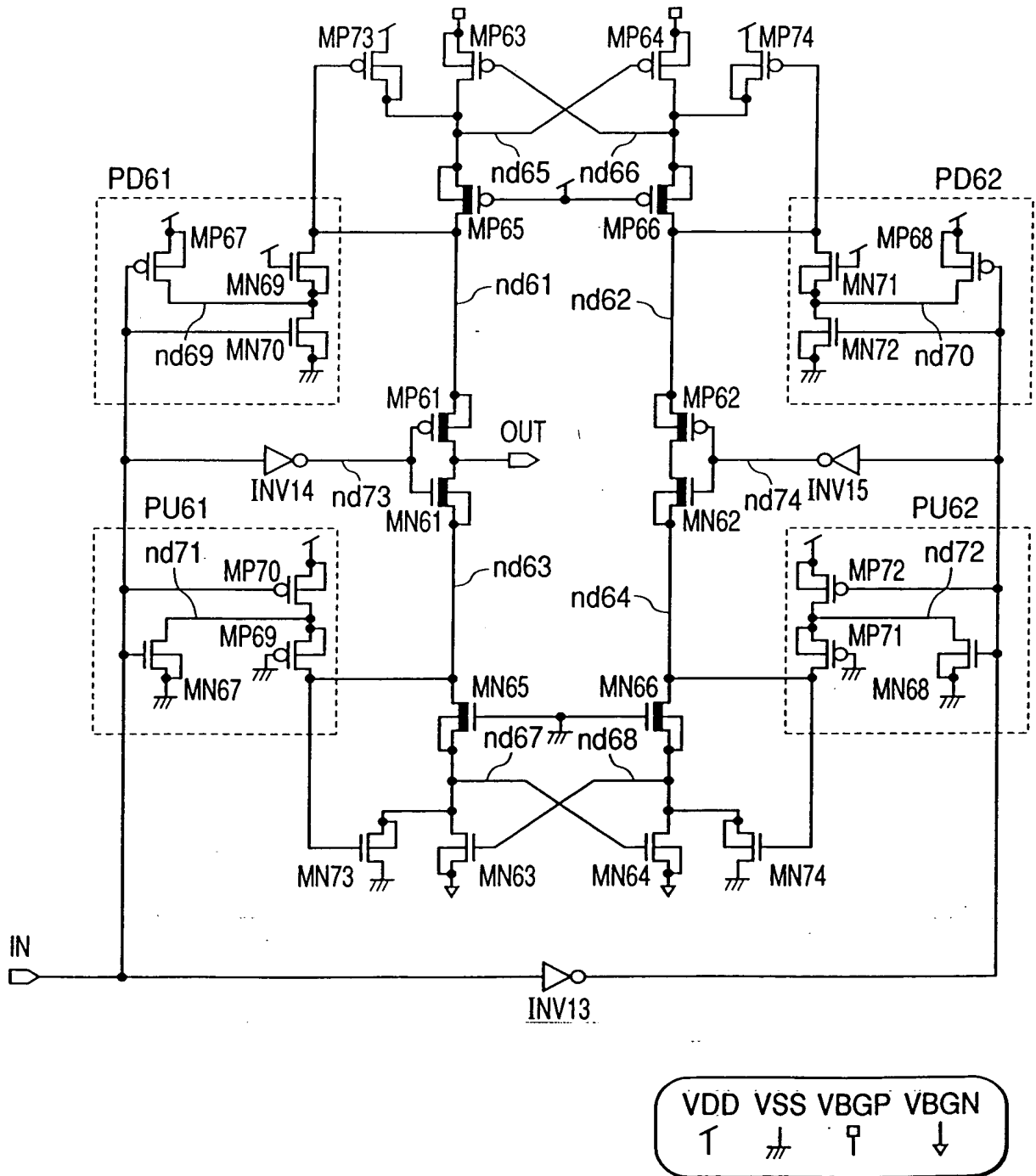
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*FIG. 15*



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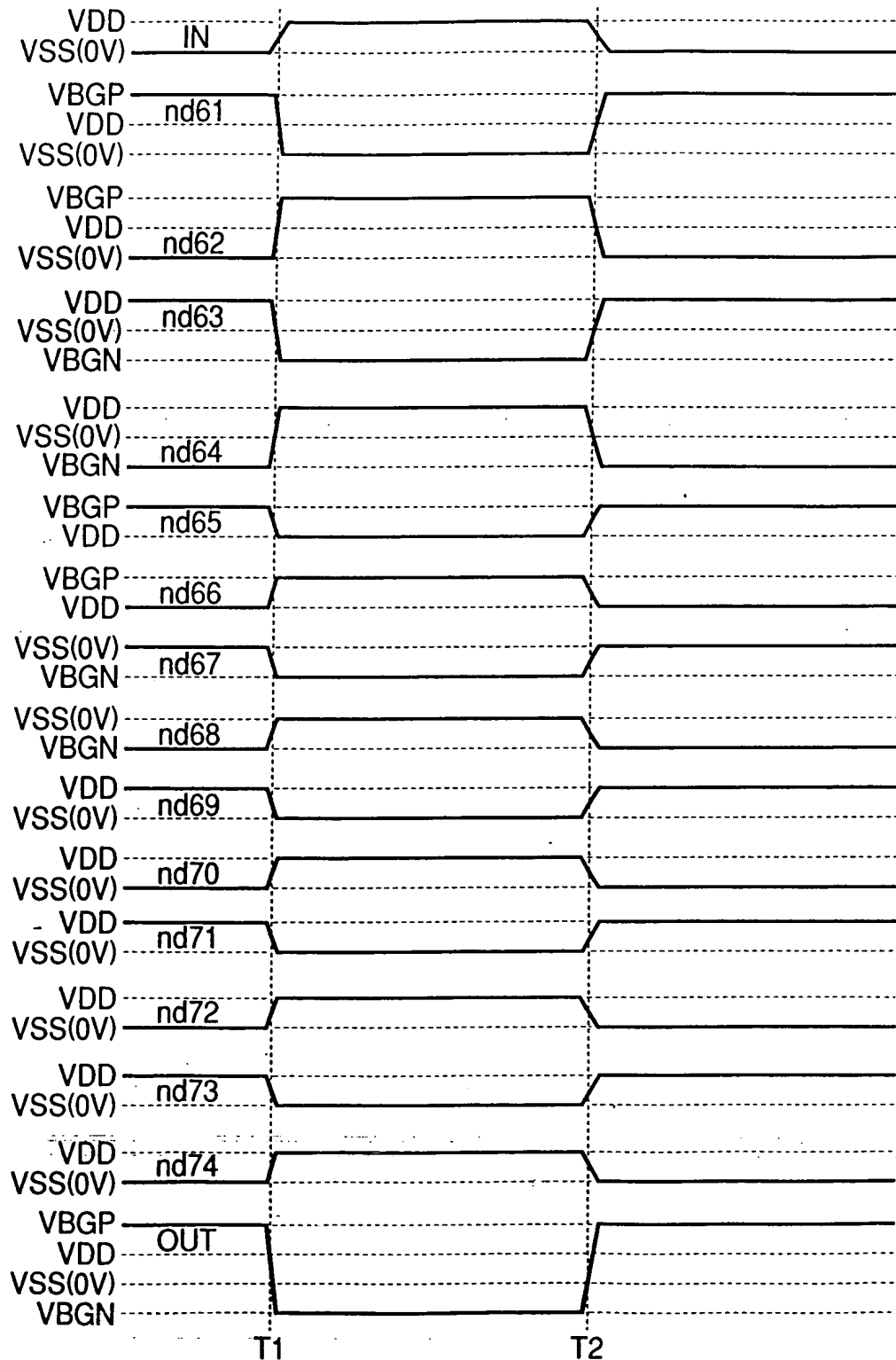
FIG. 16



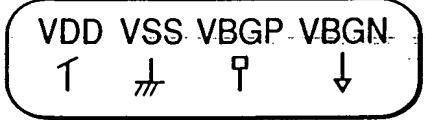


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**FIG. 17**



**FIG. 18**

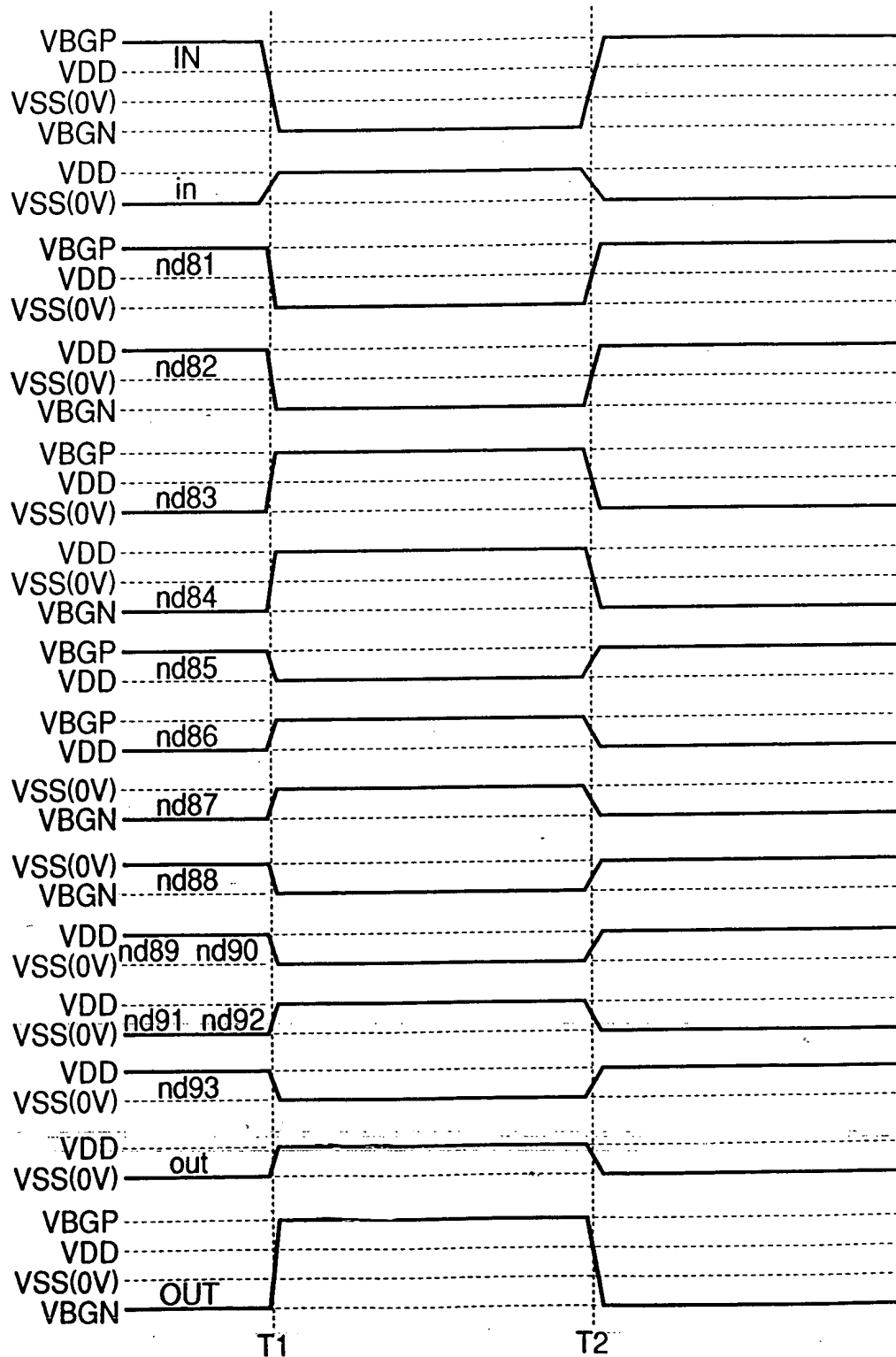


VDD VSS VBGP VBGN

↑   ↓   □   ↓

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**FIG. 19**



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FIG. 20

